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give it to you, so download it and give me a. Digital Signal And Image Processing Tamal Bose. This invention relates to a clock distribution system for distributing a high speed clock to a plurality of masters and, more particularly, to a clock distribution system capable of verifying the integrity of the clock. In the prior art, it is known to distribute a clock to a plurality of masters, such as processors, via a clock distribution ring or tree. Typically, these prior art systems require a substantially static and single point of clock reference or "crosspoint" within the clock distribution system. That is, the crosspoint in the clock distribution system is generally not dynamically reconfigured to meet changing system requirements. If the system configuration changes, such as adding a new processor or modifying an existing processor, the clock distribution system cannot automatically account for the new configuration. This can lead to significant inefficiencies, especially in the context of a wide area network (WAN). FIG. 1 shows a block diagram of a prior art crosspoint clock distribution system 100. As shown in FIG. 1, the system 100 includes multiple processors 105, a clock source 110 and a crosspoint 115. The clock source 110 provides a high speed clock CLK to the crosspoint 115 and the crosspoint 115 distributes the clock to each of the processors 105 in the system 100. The system of FIG. 1 includes both a master clock distribution bus 125 and a slave clock distribution bus 130, so the system of FIG. 1 may be thought of as a double fanout ring. The crosspoint

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